

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

M. Sahara, et al.

Application No.:

09/974,814

Date:

October 12, 2001

For:

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND

THE PROCESS OF THE SAME

Art Unit:

2814

Examiner:

T. Quach

REQUEST FOR INTERVIEW PRIOR TO A FURTHER OFFICE ACTION

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 September 7, 2004

Sir:

The concurrently filed Request for Continued Examiner (RCE) Transmittal and required Submission Under 37 CFR § 1.114 (Amendment) are noted. If any issues remain in connection with the above-identified application upon review of the Submission, and in order to facilitate proceedings in connection with the above-identified application, the Examiner is respectfully requested to contact the undersigned to schedule an interview in connection with the above-identified application, so as to resolve any remaining outstanding issues. The Examiner is thanked in advance for complying with this request.

To the extent necessary, please charge any shortage in the fees due and credit any overpayment of fees in connection with the filing of this paper to the

Application No.: 09/974,814 Docket No.: 501.40724X00

deposit account of Antonelli, Terry, Stout & Kraus, Deposit Account No. 01-2135 (Docket No. 501.40724X00).

Respectfully submitted,

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WIS/dlt

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